Status of the APENet project

Presenter: Roberto Ammendola

R. Ammendola, M. Guagnelli, R. Petronzio, D. Rossetti, A. Salamon, N. Tantalo and P. Vicini

Abstract: We present the current status of APENet, our custom 3-dimensional interconnect architecture to be used in a PC clusters environment. Updated benchmarks of bandwidth and latency are shown, according to the latest hardware/software enhancements. The low level device driver has been reworked by following a custom hardware RDMA architecture, and MPICH-VMI, an implementation of the MPI library, has been ported to APENet. Benchmarks of LQCD applications in a fully APENet-based cluster are also presented.